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**PATENT**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In the Matter of the Application of:  
William James Palmteer et al.

Examiner: Lourdes C. Cruz

Application No.: 09/966,222

Group Art Unit: 2872

Filed: September 28, 2002

Attorney Docket: 17658

For: Improved Lead Frame-Based Chip Scale Package

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Assistant Commissioner for Patents  
Washington, DC 20231

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail addressed to the Assistant Commissioner for Patents, Washington, DC 20231 on August 13, 2002.

  
Paula Capriglione

**RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

This is in response to the Office Action mailed July 30, 2002 having a shortened period for response expiring on August 30, 2002. Please amend the application as indicated below.

**REMARKS**

The Office has imposed a restriction requirement under 35 USC §121 between the following groups:

- |          |   |
|----------|---|
| Group I  | Claims 1-9, drawn to a semiconductor device; and                  |
| Group II | Claims 10-18, drawn to a method of making a semiconductor device. |

Applicants elect to prosecute the invention of Group I, claims 1-9 in the present application. The election is made without traverse. Applicants reserve the right to seek reinstatement of the subject matter of claims 10-18 once allowable product claims are defined.

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Respectfully submitted,

William James Palmteer et al.  
Applicant(s)



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Joseph A. Tessari  
Registration No. 32,177  
Attorney for Applicant(s)  
Phone: (302) 633-3566  
Facsimile: (302) 633-2776

Dated: 13 August 2002